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August 14, 2002

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*A. Weiss*  
Name

Commissioner for Patents  
Washington, DC 20231

RE: *SN 09/779,374 "SYSTEM AND METHOD FOR SENSING FABRICATING LOGIC DEVICES COMPRISING CARBON NANOTUBE TRANSISTORS" – Vladimir Mancevski (Our File No. 500929.000008)*

Sir:

Enclosed for filing in the above-referenced patent application are:

1. Information Disclosure Statement;
2. Information Disclosure Statement by Applicant (Form PTO/SB/08A) (2 pages);
3. References (C1-C19); and
4. Postcard.

No fees are believed to be due in connection with the filing of the Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to the enclosed materials, the Assistant Commissioner is hereby authorized to deduct said fees from Thompson & Knight, L.L.P.'s Deposit Account No. 20-0821/500929.000008.

Commissioner for Patents

August 14, 2002

Page 2

Please date stamp and return the enclosed postcard evidencing receipt of these materials.

Respectfully submitted,

*Aaron A. Weiss*  
Aaron A. Weiss  
Reg. No. 46,163

Encl:



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**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Vladimir Mancevski

Group Art Unit: 2812

Serial No.: 09/779,374

Examiner: Unknown

Filed: February 7, 2001

Atty. Dkt. No.: 500929.000008

Title: SYSTEM AND METHOD FOR  
FABRICATING LOGIC DEVICES  
COMPRISING CARBON NANOTUBE  
TRANSISTORS

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Information Disclosure Statement be entered and the documents listed on the attached Form PTO/SB/08A be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. §§ 1.97(g)-(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed to be

an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The present Information Disclosure Statement is being filed prior to the receipt of a first Office Action reflecting an examination on the merits, and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). Therefore, no fees are believed to be due in connection with the filing of this Information Disclosure Statement; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these materials, the Assistant Commissioner is hereby authorized to deduct said fees from Thompson & Knight L.L.P.'s Deposit Account No. 20-0821. Please reference Attorney Docket No. 500929.000008.

Applicant respectfully requests that the listed documents be made of record in the present case.

Respectfully submitted,



Aaron A. Weiss  
Reg. No. 46,163

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ATTORNEYS FOR APPLICANT

Dated: August 14, 2002



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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

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of

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**Complete if Known**

Application Number	09/779,374
Filing Date	February 7, 2001
First Named Inventor	Vladimir Mancevski
Group Art Unit	2812
Examiner Name	
Attorney Docket Number	500929.000008

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS		
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
	C1	LI, J., et al "Highly-Ordered Carbon Nanotube Arrays for Electronics Applications" Applied Physics Letters, Volume 75, no. 3 (July 19, 1999), pp. 367-369. XP-000850812.
	C2	CHICO, L., et al "Pure Carbon Nanoscale Devices: Nanotube Heterojunctions" Physical Review Letters, Volume 76, no. 6 (February 5, 1996), pp. 971-974.
	C3	COLLINS, P.G., et al "Nanoscale Electronic Devices on Carbon Nanotubes" Fifth Foresight Conference on Molecular Technology [online] [retrieved on April 23, 2002]. Retrieved from the Internet: <URL: http://www.foresight.org/Conferences/MNT05/Papers/Collins/index.html>.
	C4	TERRONES, M., et al "Controlled Production of Aligned-Nanotube Bundles" Nature, Vol. 388, (July 3, 1997), pp. 52-55.
	C5	HORNYAK, G.L., et al "Template Synthesis of Carbon Nanotubes" Fourth International Conference on Nanostructured Materials. NanoStructured Materials, Vol. 12, (1999), pp. 83-88. PIIS-0965-9773 (99) 00071-9.
	C6	MARTEL, R., et al "Single and Multi-Wall Carbon Nanotube Field-Effect Transistors" Applied Physics Letters, Volume 73, no. 17 (October 26, 1998), pp. 2447-2449. XP-000996900.
	C7	VEDENEV, A.S., et al " Molecular-Scale Rectifying Diodes Based on Y-Junction Carbon Nanotubes" International Electronic Devices Meeting, (1999), XP-001004406.
	C8	LI, J., et al " Growing Y-Junction Carbon Nanotubes" Nature, Vol. 402, (November 18, 1999), pp. 253-254.
	C9	MENON, M., et al " Fullerene-Derived Molecular Electronic Devices" Semiconductor Science and Technology, Vol. 13, (1998), pp. A51-A54. XP 000768865.
	C 10	SAITO, S., " Carbon Nanotubes for Next-Generation Electronics Devices" Science, Vol. 278, (October 3, 1997), pp. 77-78
	C 11	SUENAGA, K., et al "Synthesis of Nanoparticles and Nanotubes with Well-Separated Layers of Boron Nitride and Carbon" Science, Vol. 278, (October 24, 1997), pp. 653-655.

Examiner Signature	Date Considered
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Filing Date	February 7, 2001
First Named Inventor	Vladimir Mancevski
Group Art Unit	2812
Examiner Name	

Attorney Docket Number 500929.000008

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### OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	C 12	VAN HAESENDONCK, C., et al "Nanowire Bonding with the Scanning Tunneling Microscope" Surface Science, 386, (1997), pp. 279-289. PII 50039-6028 (97) 00307-5.	
	C 13	TANS, S.J., et al "Room-Temperature Transistor Based on A Single Carbon Nanotube" Nature, Vol. 393, (May 7, 1998), pp. 49-52.	
	C 14	HU, J., et al " Controlled Growth and Electrical Properties of Heterojunctions of Carbon Nanotubes and Silicon Nanowires" Nature, Vol. 399, (May 6, 1999), pp. 48-51.	
	C 15	BARD, A.J., et al "Bulk Electrolysis Methods" Electromechanical Methods: Fundamentals and Applications (New York, John Wiley & Sons, Inc., 1980), pp. 370-428.	
	C 16	OHJI, H., et al "Macro Porous Silicon Formation for Micromachining" Micromachining and Microfabrication Process Technolgy III, (September 29-30, 1997), Proceedings of SPIE, Vol. 3223, pp. 189-197.	
	C 17	BOMCHIL, G., et al "Porous Silicon: Material Properties, Visible Photo and Electroluminescence" Proceedings of the Sixth International Conference on Solid Films and Surfaces, June 29 - July 3, 1992, pp. 394-407.	
	C 18	LEHMANN, V., et al "Formation Mechanism and Properties of Electrochemically Etched Trenches in n-Type Silicon" J. Electrochem. Soc., Vol. 137, No.2, (February 1990), pp. 653-659.	
	C 19	CHE, G., et al "Chemical Vapor Deposition Based Synthesis of Carbon Nanotubes and Nanofibers Using a Template Method" [online] Chem. Mater., Vol. 10, 1998, pp. 260-267. Retrieved from the Internet: <URL: <a href="http://bucky-central.mech.edu/Rnoffs/76.pdf">http://bucky-central.mech.edu/Rnoffs/76.pdf</a> >.	
	C 20		
	C 21		
	C 22		

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